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RCI/DNS/PROJ/24/06/01

21 June 2024

CERTIFICATE

This is to certify that **Mr. CH. SUSHANTH (Roll No. A22126512083) B.Tech 2nd Year 2nd Semester in Electronics & Communications Engineering student from Anil Neerukonda Institute of Technology and Sciences, Sangivalsa, Bhemili, Visakhapatnam, Andhra Pradesh** has successfully completed his Internship Training on "**Implementation of Basic Logic Circuits using FPGA**" from 20th May 2024 to 20th June 2024 at **Directorate of Navigation System, Research Centre Imarat, DRDO** under the guidance of **Mr. PRAVEEN TIWARI, Sc 'D'**. His performance was good and I wish him success for future endeavors.




(PRAVEEN TIWARI)
Sc 'D'
DNS/RCI

वैज्ञानिक / Scientist
निदेशालय, नाविगेशन एवं एम्बिडेड कंप्यूटर्स
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