



Certificate of Participation

This is to certify that **Vempala Purna Satya Srínívas** of **Aníl Neerkonda Instítute of Technolgy and Scíences** has successfully completed the Internship Program on

FPGA Design and Verification

held from 14th June to 13th July 2021, from

Sandeepaní School of Embedded System Desígn, Bangalore.

Ms. Sadiya Arshad Director Education and Skill Development



www.sandeepani-training.com