

इलक्ट्रानिक्स कारपारशन आफ दुण्डिया लिमिट Electronics Corporation of India Limited CIN U321001 G1947 G01001149



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PROJECT / INTERNSHIP COMPLETION CERTIFICATE

Date: 27/07/2019

This is to certify that Mr. V. SAI SANDEEP bearing Roll.No:316126512055, a student of ANIL NEERUKONDA INSTITUTE OF TECHNOLOGY AND SCIENCE, VISAKHAPATNAM pursuing the Degree of B.Tech., in Electronics and Communication Engineering, has carried out Project work/Internship titled "DESIGN AND IMPLEMENTATION OF VEDIC MULTIPLIER ON FPGA" in VLSI under our guidance during the period from 06 May, 2019 to 05 June, 2019 in partial fulfillment of the requirements for the award of the above mentioned Degree. The student is punctual, hardworking and shown keen interest to produce the project output and results.

SRIDHARA SHETTY

AGM: CED

SRIDHARA S AGM, CED

ECIL, HYDERABAD-500 062

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अति ि युद्ध कास्त्रोकस, ई सी ार्ग्य जिल्ला, तेलंगाण भारत, Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA. दरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017 वेद पाइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in